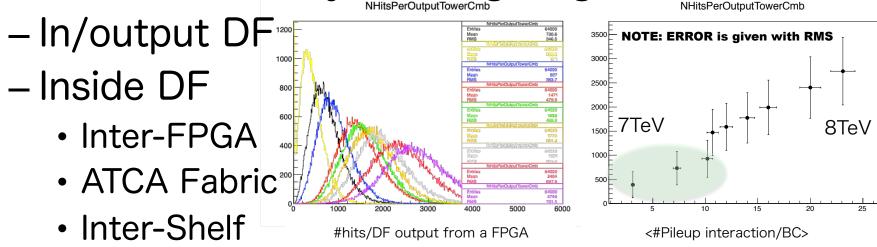
## Status report (Yasu)

## Cabling (Module-ROD-DF-FTK Tower)

 Determine a "draft" of ROD-DF connection (222 ROD vs 64 FPGA)

Data volume study is on-going



After obtaining well solid, expand them to further cabling fine tunes study or hardware requirement (i.e. bandwidth, buffer size etc.)